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<u>L31</u>	L25 and l10	2	<u>L31</u>
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<u>L27</u>	L25 and l6	55	<u>L27</u>
<u>L26</u>	L25 and l5	65	<u>L26</u>
<u>L25</u>	L24 near80 (analog\$5 or digital)	2111	<u>L25</u>
<u>L24</u>	(mux or multiplex\$5 or select\$6 or crossbar\$1 or rout\$4) near55 L23	72965	<u>L24</u>
<u>L23</u>	(configur\$6 or reconfigur\$5) near15 (element\$1 or fpga\$1 or pla\$1 or cell\$1 or block\$1 or function\$3 or pld\$1 or unit\$1)	755250	<u>L23</u>
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<u>L21</u>	14 and 19	12	<u>L21</u>
<u>L20</u>	14 and 18	218	<u>L20</u>
<u>L19</u>	14 and 17	62	<u>L19</u>
<u>L18</u>	14 and 16	171	<u>L18</u>
<u>L17</u>	14 and 15	234	<u>L17</u>
<u>L16</u>	13 and 110	31	<u>L16</u>
<u>L15</u>	13 and 19	214	<u>L15</u>
<u>L14</u>	13 and 18	670	<u>L14</u>
<u>L13</u>	13 and 17	214	<u>L13</u>
<u>L12</u>	13 and 16	431	<u>L12</u>
<u>L11</u>	13 and 15	613	<u>L11</u>
<u>L10</u>	(718/106-108)![CCLS]	1496	<u>L10</u>
<u>L9</u>	(709/220-222)[CCLS]	4127	<u>L9</u>
<u>L8</u>	(716/2-17)[CCLS]	9713	<u>L8</u>
<u>L7</u>	(710/305-317)![CCLS]	5622	<u>L7</u>
<u>L6</u>	(712/2-39,225-229)[CCLS]	7061	<u>L6</u>
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<u>L3</u>	L2 and (analog\$5 or digital)	30351	<u>L3</u>
<u>L2</u>	(mux or multiplex\$5 or select\$6 or crossbar\$1 or rout\$4) near55 11	58170	<u>L2</u>
<u>L1</u>	(configur\$6 or reconfigur\$5) near15 (fpga\$1 or pla\$1 or cell\$1 or block\$1 or function\$3 or pld\$1 or unit\$1)	579298	<u>L1</u>

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